

#3  
3.26.02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

U.S. PRO  
JC857 09/966841



Applicants: Thomas P. Glenn, Steven Webster  
Assignee: Amkor Technology, Inc.  
Title: Integrated Circuit Package Including Interconnection Posts For Multiple Electrical Connections  
Serial No.: Not Yet Assigned Filing Date: Herewith  
Examiner: Not Yet Assigned Group Art Unit: Not Yet Assigned  
Docket No.: M-9225 US

San Jose, California  
September 27, 2001

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the document listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. A copy of this document is enclosed.

Citation of this document shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

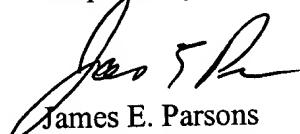
LAW OFFICES OF  
SKJERVEN MORRILL  
MacPHERSON LLP

25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979

Applicants submit that no fee is required. If, however, a fee is necessary under 37 C.F.R. 1.97(c), then such fee and any other appropriate fee may be charged to Deposit Account No. 19-2386.

EXPRESS MAIL LABEL NO:  
EL 901 564 011 US

Respectfully submitted,



James E. Parsons  
Attorney for Applicants  
Reg. No. 34,691

LAW OFFICES OF  
SKIERVEN MORRILL  
MacPHERSON LLP

25 METRO DRIVE  
SUITE 700  
SAN JOSE, CA 95110  
(408) 453-9200  
FAX (408) 453-7979